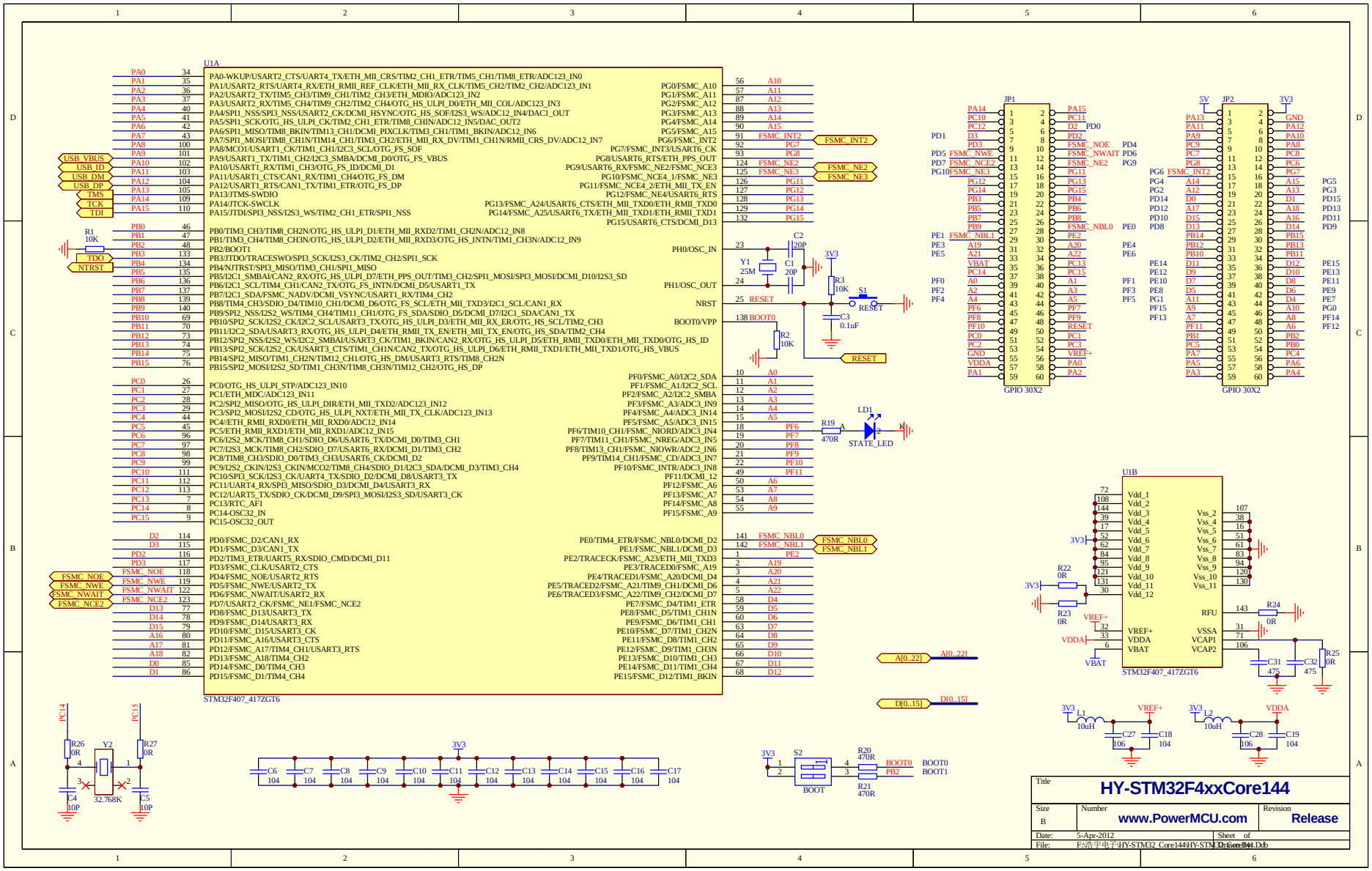
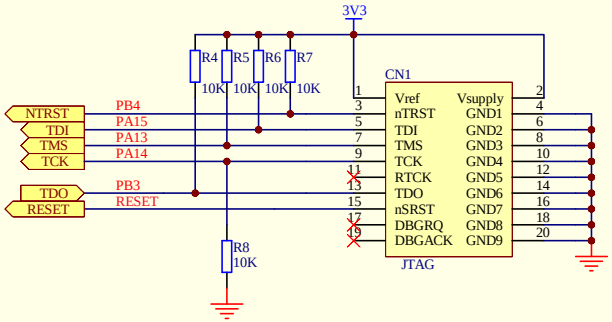
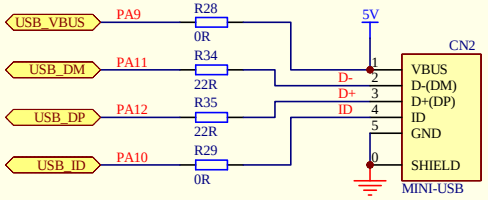

U1A
STM32F407_417ZGT6
JP1
JP2
GPIO 30X2
U1B
STM32F407_417ZGT6
Title
HY-STM32F4xxCore144
Size
B
Number
www.PowerMCU.com
Revision
Release
Date: 5-Apr-2012
File: F:\浩宇电子\HY-STM32_Core144\HY-STM32_Core144.Ddb

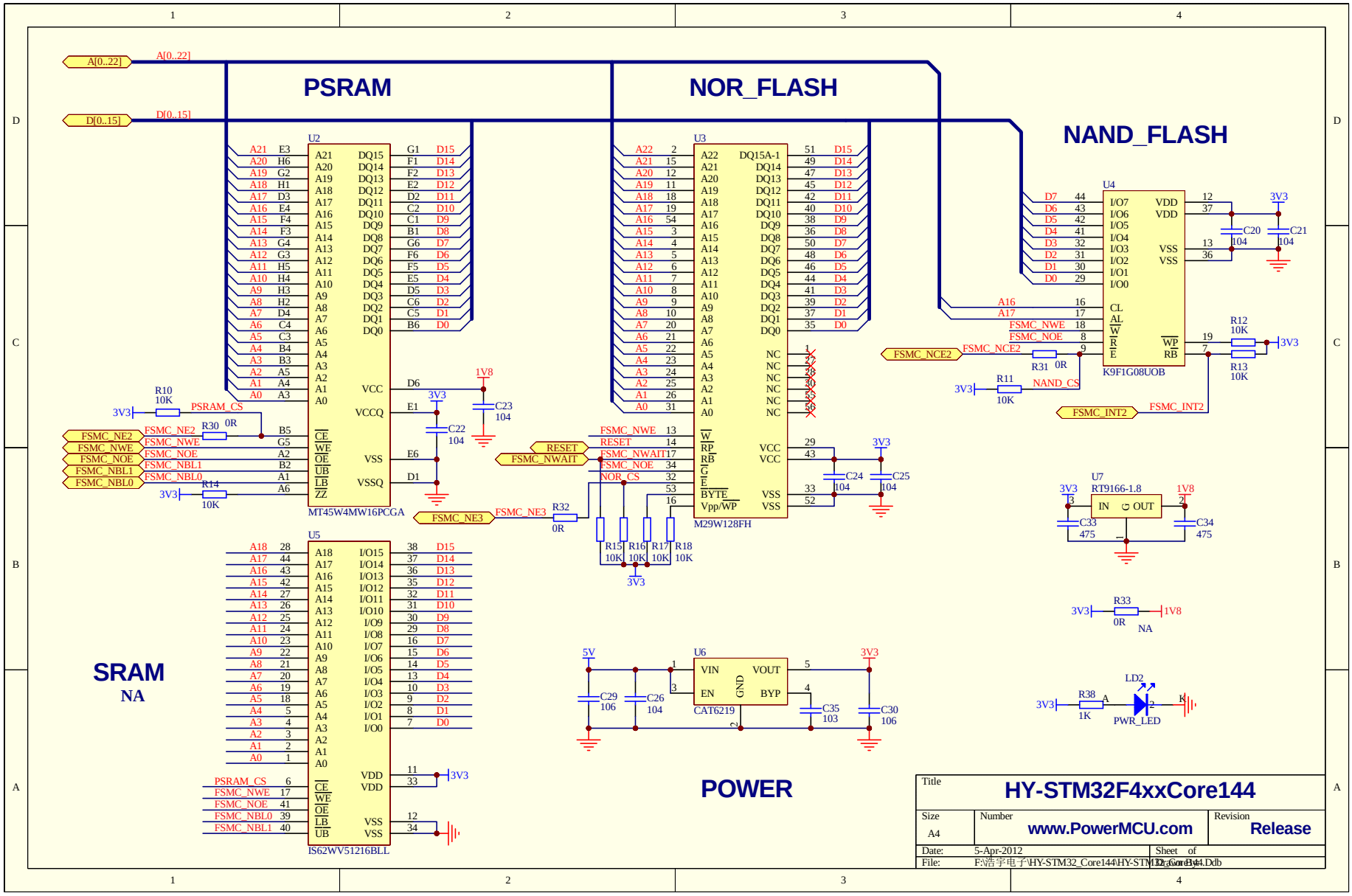


JTAG



USB

Title	HY-STM32F4xxCore144		
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Date: 5-Apr-2012		Sheet of	
File: F:\浩宇电子\HY-STM32_Core144\HY-STM32_Core144.Ddb		Drawn By:	


PSRAM
NOR_FLASH
NAND_FLASH
SRAM
NA
POWER
A[0..22]
D[0..15]
U2
MT45W4MW16PCGA
U3
M29W128FH
U4
K9F1G08UOB
U5
IS62WV51216BLL
U6
CAT6219
U7
RT9166-1.8
PSRAM_CS
FSMC_NE2
FSMC_NE3
FSMC_NWE
FSMC_NOE
FSMC_NBL0
FSMC_NBL1
FSMC_NCE2
FSMC_INT2
NAND_CS
RESET
FSMC_NWAIT
NOR_CS
3V3
1V8
5V
PWR_LED
Title
HY-STM32F4xxCore144
Size
A4
Number
www.PowerMCU.com
Revision
Release
Date:
5-Apr-2012
File:
F:\浩宇电子\HY-STM32_Core144\HY-STM32_Core144.Ddb